

National Institute of Technology Meghalaya

CURRICULUM

An Institute	of National	Importance
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Programme Department		ne	e Bachelor of Technology in Electronics and Communication Engineering									Year of Regulation				2018-19	
		nt Electronics and Communication Engineering												IV			
Co	urse		Credit									Structure Marks Dist				ution	
Code		Course Name								L	Т	PCCONTINOU EVALUATION2270		CONTINOUS EVALUATION	VIVA	То	tal
EC 256 Microprocessors and Microc						ocontroll	controllers Lab			1	70			30	1(100	
		To understand principles and microlevel operation of CO1 Ability to understand the basic con and instruction execution											cepts of pro	ocessors, c	ontrolle		
		To develop the skills for programme the processors with low and high level programming languages									CO2	Ability to apply assembly and high level languages to program processors and controllers					
Course	urse	Course Course Outcomes CO3 Ability to apply interfacing proce like, I/O, A/D, D/A, timer etc										interfacing proces A, timer etc	essor/controller with peripheral				
objectives										CO4	Ability to design real time applications using different microcontrollers						
								CO5									
											CO6						
No	COs						Mapp	ing with Pr	ogram (Outcomes (P	Os)	1	1		Map	lapping with PSOs	
	005	PC	01	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO:
1	CO1	3	;	3	-	2	3	-	1	-	-	-	-	-	-	-	-
2	CO2	3	;	2	-	2	3	-	1	-	-	-	-	-	-	-	-
3	CO3	3	;	1	-	2	3	-	1	-	-	-	-	-	-	-	-
4	CO4	3	;	1	-	2	3	-	1	-	-	-	-	-	-	-	-
5	CO5	-		-	-	-	-	-	-	-	-	-	-	-	-	-	-
6	CO6	<u>)6</u>										- -					
No								Contont	. 511	LABUS					Hours		COa
INU.	8085/8	Re Ras	ad pro	aromm	ing (A 664	mbly).		Content							Tiours		cos
	0005/0	 Program set for Logical and Decimal. 															
Ι	0	• Program set for Subroutines and Delay.										04		CO1			
	0	o Interfacing															
	8051 E	Based system programming (Assembly and C):															
	0	> Arithmetical and logical															
	0	> I/O port interfacing.															
п	0	Timer based system											12				
11	0	> Interrupt generation										12		<i>2</i> , co:			
	0	Display and Keyboard interfacing,															
	0	Serial	Comm	unicatio	on												
	0	Senso	or interfa	acing													
III	PIC B	Based system programming (Assembly and C):: I/O port interfacing.															
	0	Timer	based s	system.											04	CC)3, CO
	0	 Display/Keyboard interfacing 															
							Тс	otal Hours							20		
Esser	ntial Rea	adings													-	<u> </u>	
	Case	lion D	S "M	licropro	cessor A	chitecture	Program	nming and	1 Appli	cations with	8085"	Donrom In	ternation	al Fifth edition	1000		

Supplementary Readings

- 1. M.K. Patel "The 8051 Microcontrollers based Embedded Systems", MCGraw Hill, 2014
- 2. Hall D., "Microprocessors and Interfacing : Programming and Hardware", Tata McGraw-Hill, 1992
- 3. Wilmshurst, T. "Designing Embedded Systems With PIC Microcontrollers : Principles and Applications", Elsevier (Newnes) Second Edition, 201