AND THE OF TECHNOLOGY AND		National Institute of Technology Meghalaya An Institute of National Importance													CURRICULUM		
Progra	amme	ne Master of Computer Applications Year of R											egulation	on 2024-25			
Depart	tment	1 11										Semester			III		
Course	9	Course Name Pre-Requisit								Credit S	tructure			Marks D	Distribution		
Code								Requisite	L	Т	Р	С	INT	MID	END	Total	
CA505		Operating Systems							3	0	0	3	50	50	100	200	
										CO's		State	ement		Bloom's	s Taxonor	
	To	o introdu	ce the comp	onents of op	erating syste	em				CA505.1	Able to learn the fundamentals of Open			erating	ting Understand		
	To	To analyse the process scheduling and execution								CA505.2	Able to acquire knowledge about diffe			ferent process	ent process Understand		
	To	To describe the structure of main memory, virtual memory								CA505.3	scheduling techniques. Able to solve process synchronization			n and			
Course	-	, i									deadlock handling strategies				Арріу		
Objective		To describe the function of file systems							Outcomes	CA505.4	memory management techniques and replacement algorithms.			d page	Analyse		
	l l	To explore the structure of an operating system's I/O subsystem and hardware.								CA505.5				alyse various			
COs		Mapping with Program Outcomes (POs)											Мар	ping with	PSOs		
COS	I	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	
CA505.		2												2	1		
CA505.2		2	1	1	1				1			1	1	2	1	1	
CA505.3		2	2	2	1								1	2	1	1	
CA505.4		2	2	2	2					1		1	1	1	1	1	
CA505.5		1		1	1								1	1	1		
CA505		1.80	1.67	1.50	1.25			0)//	1.00	1.00		1.00	1.00	1.60	1.00	1.00	
NI.							0		LLABUS					Harring	T	00-	
No.	Introd	duction	n				Cont	ent						Hours	COs CA505.1		
	Opera	troduction perating Systems Functionalities - Formal Definition - Evolution – Types of operating system, Services, perating system Design and Implementation, Operating System Structure.											04	OA000.1			
II	Proce IPC n Proce Monit	Process Management Process Concept - Process control block, Process Hierarchy, Threads – Single Thread and Multi Thread Model, PC models: shared memory and message passing. CPU Scheduling algorithms, Multiprocessor Scheduling, Process Synchronization - Peterson's Solution, Process Synchronization - Semaphores, Critical Regions, Monitors - Deadlock prevention- Deadlock avoidance and Deadlock Detection and Recovery - Bankers gorithm.											14	CA505.2, CA505.3			
III	Memory Management Overview of Swapping - Multiple Partitions – Paging, Page table, Segmentation, Demand paging- Fragmentation & Compaction- Page replacement algorithms, Memory allocation algorithms: first fit, Best fit, worst fit.													12	CA505.1, CA505.4		
	Acce	File System Access Methods, Contiguous-Sequential and Indexed Allocation, File system interface - File System implementation, Secondary Storage Structure.											System	08	CA505.1, CA505.5		
	RAID	ystem -disk ch table	scheduli	ng- Devi	ce driver	s - block	and cha	aracter c	devices-stre	ams, Cha	aracter ar	nd Block	device	04		A505.1, A505.5	
						Т	otal Hour	'S						42			

Essential Readings

- 1. Abraham Silberschatz, Peter Baer Galvin, Greg Gagne, "Operating System Concepts", 9th Edition, John Wiley & Sons Inc. 2012
- 2. Andrew S Tanenbaum, "Modern Operating Systems", 4th Edition, Prentice Hall. 2014
- 3. William Stallings, "Operating System: Internals and Design Principles", 9th Edition, Pearson, 2018.

Supplementary Readings

- 1. Harvey M. Deitel, Paul J. Deitel, David R. Choffnes, "Operating System", 3rd Edition, Pearson, 2013.
- 2. D M Dhamdhere, "System Programming and Operating Systems", 2nd Edition, Tata McGraw Hill, 2009.
- 3. Gary Nutt, "Operating Systems: A Modern Perspective", 2nd Edition, Addison Wesley, 2001.
- 4. Achyut S Godbole, "Operating Systems", 3rd Edition, Tata McGraw Hill, 2010.