OF THICHNOLOGY

National Institute of Technology Meghalaya

An Institute of National Importance

CURRICULUM

	TUTE OF TECHNOLS	90																
Programi		me Bachelor of Technology in Electronics and Communication Engineering										Year of Regulation				2018		
Departm		ent Electronics and Communication Engineering									Semester				I/II			
Cou	urse	Credit Stru								ıcture			Marks Distribution					
Code		Course Name							L	Т	Р	С	Contin	uous Eva	ous Evaluation Total			
EC	C 151	Basic Electronics Engineering Lab							0	0	2	1		100 100)0	
Course Objectives		To develop the student's ability to apply the basic principles of electronics in circuit designing.								CO1	Verify the V-I characteristics of p-n junction diode, schottky diode, zener diode (Voltage Regulation), LED and study of rectifier and filtering Circuits.							
		To develop the student's ability to design circuits based on diode, transistor and digital logic ICs.								CO2	Study the characteristics and switching action of BJT in CE, CBand CC mode.							
		To develop the student's ability to communicate effectively the knowledge of electronics and communication systems.								CO3	Interpret the truth tables of logic gates and Demorgan's theorems for digital electronics circuits.							
		CO4 Work in teams to plan a Digital systems.										o plan an	and execute the creation of complex					
No.	COs	Mapping with Program Outcomes (POs)													Mapping with PSOs			
		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4	
1	CO1	3	1	1	0	1	0	0	1	1	0	0	0	3	0	3	3	
2	CO2	3	1	1	0	1	1	1	2	1	0	0	0	2	0	2	3	
3	CO3	3	2	2	1	1	1	1	2	1	0	0	0	2	3	2	3	
4	CO4	1	1	1	1	0	3	3	1	1	3	0	0	2	3	2	3	
								SYI	LLABUS									
No.		List of Experiments													Hours COs			
Ι	I-V cha	racteristi	ics of forwar	d biased P-	N junction	Diode									CO1	, CO2, CC)3 &CO4	
II	Reverse	e charact	eristics of Z	ener Diode														
III	Zener I	Diode as	a reference l	Diode.														
IV	Half-wa	ave rectil	tier using die	ode														
V	Full-wa	ave rectifier using diode																
VI	Bridge	ge rectifier.																
VII	Truth T	able veri	ification of I	logic Gates.														
VIII	Design	of basic	logic gates u	ising NANI) & NOR g	gates												
IX	Input &	c output o	characteristi	es of BJT in	CB mode													
X	Input &	output c	characteristi	es of BJT in	CE mode													
	Total Hours																	
Esser	ntial Rea	adings																
1	. Basic	Electro	nics, Chatt	opadhyay &	¢ Rakshit	, New Age	e Publishe	er, 2009										
Supp	lementa	ary Read	lings															
1	. Electr	onics Pri	inciples, Alt	ert P. Malv	ino, Publis	her: Tata N	1cGraw-Hi	ill, 2010										
2	2. Electr	onics De	evices, Thon	as L. Floyd	, Publisher	: Pearson l	Education,	2008										